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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	§	Art Unit:	2186
Ramesh V. Peri et al.	§		
Serial No.:	§	Examiner:	Paul W. Schlie
10/717,085	§		
Filed:	§	Docket:	ITL.1059US P17918
November 19, 2003	§		
For:	§	Assignee:	Intel Corporation
Accessing Data From Different Memory Locations in the Same Cycle	§		

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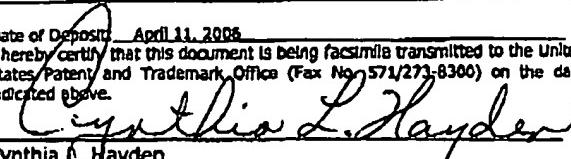
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Sir:

REPLY TO FINAL REJECTION

In response to the final rejection mailed March 16, 2006, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made.

Date of Deposit: April 11, 2006
I hereby certify that this document is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 571/273-8300) on the date indicated above.

Cynthia L. Hayden